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We hereby revoke all previous powers of attorney given in the US applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399 and appoint the practitioners associated with the Customer Number 25570 to act on our behalf for each of the identified US applications and/or patents recorded by U.S. Patent and Trademark Office on July 13, 2009 in Reel/Frame 022939/0399.

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Patent Owner, Microelectronic Technology, Inc., is the Assignee of record of the entire interest. A Statement under 37 C.F.R. 3.73(b) is enclosed. The undersigned is authorized to act on behalf of the assignee.

Signature



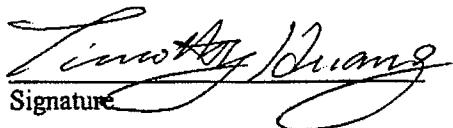
7-16-2009
Date

Mr. Timothy HUANG
Authorized representative of
MICROELECTRONICS TECHNOLOGY, INC.

IN THE UNITED STATES PATENT & TRADEMARK OFFICE
STATEMENT UNDER 37 CFR 3.73(b)

Patent Owner, Microelectronic Technology, Inc., a corporation, states that it is the assignee of the entire right, title, and interest in the U.S. patent applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399.

The undersigned is authorized to act on behalf of the assignee.


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Date

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Patent Assignment Details

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Attorney Dkt #: 14485.13

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Total properties: 79

	Patent #:	Issue Dt:	Application #:	Filing Dt:
1	4941153	07/10/1990	07089281	08/25/1987
	Title: HIGH-SPEED DIGITAL DATA COMMUNICATION SYSTEM			
2	4975931	12/04/1990	07286435	12/19/1988
	Title: HIGH SPEED PROGRAMMABLE DIVIDER			
3	5058107	10/15/1991	07293894	01/05/1989
	Title: EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER			
4	5130578	07/14/1992	07444454	11/30/1989
	Title: EFFICIENT HIGH SPEED N-WORD COMPARATOR			
5	5128534	07/07/1992	07554238	07/17/1990
	Title: HIGH CHARGE CAPACITY FOCAL PLANE ARRAY READOUT CELL			
6	5271038	12/14/1993	07580710	09/10/1990
	Title: DISTORTION SUPPRESSION USING THRESHOLDING TECHNIQUES			
7	5303161	04/12/1994	07624958	12/10/1990
	Title: TECHNOLOGY INDEPENDENT INTEGRATED CIRCUIT MASK ARTWORK GENERATOR			
8	5164959	11/17/1992	07643969	01/22/1991
	Title: DIGITAL EQUALIZATION METHOD AND APPARATUS			
9	5267272	11/30/1993	07655684	02/14/1991
	Title: A RECEIVER AUTOMATIC GAIN CONTROL (AGC)			
10	5136205	08/04/1992	07675590	03/26/1991
	Title: MICROELECTRONIC FIELD EMISSION DEVICE WITH AIR BRIDGE ANODE			
11	5128674	07/07/1992	07676635	03/28/1991
	Title: TWO QUADRANTS HIGH SPEED MULTIPLYING DAC			
12	5206647	04/27/1993	07722763	06/27/1991
	Title: LOW COST AGC FUNCTION FOR MULTIPLE APPROXIMATION A/D CONVERTERS			

13	Patent #: <u>5251218</u>	Issue Dt: 10/05/1993	Application #: 07739593	Filing Dt: 07/31/1991
	Title: EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER			
14	Patent #: <u>5220557</u>	Issue Dt: 06/15/1993	Application #: 07765157	Filing Dt: 09/23/1991
	Title: MULTIPLE USE DIGITAL TRANSMITTER/TRANSCEIVER WITH TIME MULTIPLEXING			
15	Patent #: <u>5304951</u>	Issue Dt: 04/19/1994	Application #: 07829183	Filing Dt: 01/31/1992
	Title: DIVIDER SYNCHRONIZATION CIRCUIT FOR PHASE-LOCKED LOOP FREQUENCY SYNTHESIZER			
16	Patent #: <u>5313113</u>	Issue Dt: 05/17/1994	Application #: 07870369	Filing Dt: 04/17/1992
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17	Patent #: <u>5250911</u>	Issue Dt: 10/05/1993	Application #: 07871861	Filing Dt: 04/20/1992
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18	Patent #: <u>5315169</u>	Issue Dt: 05/24/1994	Application #: 07894980	Filing Dt: 06/08/1992
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19	Patent #: <u>5278837</u>	Issue Dt: 01/11/1994	Application #: 07905965	Filing Dt: 06/29/1992
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20	Patent #: <u>5350952</u>	Issue Dt: 09/27/1994	Application #: 07909286	Filing Dt: 07/06/1992
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21	Patent #: <u>5315231</u>	Issue Dt: 05/24/1994	Application #: 07976760	Filing Dt: 11/16/1992
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22	Patent #: <u>5621730</u>	Issue Dt: 04/15/1997	Application #: 07986180	Filing Dt: 12/07/1992
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23	Patent #: <u>5483150</u>	Issue Dt: 01/09/1996	Application #: 08017200	Filing Dt: 02/05/1993
	Title: TRANSISTOR CURRENT SWITCH ARRAY FOR DIGITAL-TO-ANALOG CONVERTER (DAC) INCLUDING BIAS CURRENT COMPENSATION FOR INDIVIDUAL TRANSISTOR CURRENT GAIN AND THERMALLY INDUCED BASE-EMITTER VOLTAGE DROP VARIATION			
24	Patent #: <u>5343163</u>	Issue Dt: 08/30/1994	Application #: 08080269	Filing Dt: 06/21/1993
	Title: SINGLE-ENDED AND DIFFERENTIAL TRANSISTOR AMPLIFIER CIRCUITS WITH FULL SIGNAL MODULATION COMPENSATION TECHNIQUES WHICH ARE TECHNOLOGY INDEPENDENT			
25	Patent #: <u>5428305</u>	Issue Dt: 06/27/1995	Application #: 08129939	Filing Dt: 09/30/1993
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26	Patent #: <u>5410274</u>	Issue Dt: 04/25/1995	Application #: 08210269	Filing Dt: 03/17/1994
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27	Patent #: <u>5592181</u>	Issue Dt: 01/07/1997	Application #: 08443519	Filing Dt: 05/18/1995
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28	Patent #: <u>5572220</u>	Issue Dt: 11/05/1996	Application #: 08443537	Filing Dt: 05/18/1995
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29	Patent #: <u>5581213</u>	Issue Dt: 12/03/1996	Application #: 08479284	Filing Dt: 06/07/1995
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30	Patent #: <u>5848160</u>	Issue Dt: 12/08/1998	Application #: 08603673	Filing Dt: 02/20/1996
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31	Patent #: <u>5729576</u>	Issue Dt: 03/17/1998	Application #: 08641452	Filing Dt: 04/30/1996
	Title: INTERFERENCE CANCELING RECEIVER			
32	Patent #: <u>5684435</u>	Issue Dt: 11/04/1997	Application #: 08653930	Filing Dt: 05/22/1996
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33	Patent #: <u>5856760</u>	Issue Dt: 01/05/1999	Application #: 08745070	Filing Dt: 11/07/1996
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34	Patent #: <u>5774318</u>	Issue Dt: 06/30/1998	Application #: 08753647	Filing Dt: 11/27/1996
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35	Patent #: <u>5960040</u>	Issue Dt: 09/28/1999	Application #: 08761103	Filing Dt: 12/05/1996
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36	Patent #: <u>5870402</u>	Issue Dt: 02/09/1999	Application #: 08764808	Filing Dt: 12/12/1996
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37	Patent #: <u>5859568</u>	Issue Dt: 01/12/1999	Application #: 08827854	Filing Dt: 04/11/1997
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38	Patent #: <u>5859558</u>	Issue Dt: 01/12/1999	Application #: 08827855	Filing Dt: 04/11/1997
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39	Patent #: <u>5859569</u>	Issue Dt: 01/12/1999	Application #: 08843200	Filing Dt: 04/14/1997
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40	Patent #: <u>6040731</u>	Issue Dt: 03/21/2000	Application #: 08848930	Filing Dt: 05/01/1997
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41	Patent #: <u>5859559</u>	Issue Dt: 01/12/1999	Application #: 08903657	Filing Dt: 07/31/1997
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42	Patent #: <u>6118811</u>	Issue Dt: 09/12/2000	Application #: 08903807	Filing Dt: 07/31/1997
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43	Patent #: <u>5990815</u>	Issue Dt: 11/23/1999	Application #: 08941457	Filing Dt: 09/30/1997
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44	Patent #: <u>5995535</u>	Issue Dt: 11/30/1999	Application #: 08965251	Filing Dt: 11/06/1997
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45	Patent #: <u>5926123</u>	Issue Dt: 07/20/1999	Application #: 08986942	Filing Dt: 12/08/1997
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46	Patent #: <u>5973631</u>	Issue Dt: 10/26/1999	Application #: 09009612	Filing Dt: 01/20/1998
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47	Patent #: <u>5963094</u>	Issue Dt: 10/05/1999	Application #: 09027241	Filing Dt: 02/20/1998
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48	Patent #: <u>6157224</u>	Issue Dt: 12/05/2000	Application #: 09219759	Filing Dt: 12/23/1998
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	Patent #: 6636730	Issue Dt: 10/21/2003	Application #: 09220288	Filing Dt: 12/23/1998
49	Publication #: US20020055347	Pub Dt: 05/09/2002		
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50	Patent #: 6891424	Issue Dt: 05/10/2005	Application #: 09408114	Filing Dt: 09/29/1999
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51	Patent #: 6552343	Issue Dt: 04/22/2003	Application #: 09574123	Filing Dt: 05/18/2000
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52	Patent #: 6931083	Issue Dt: 08/16/2005	Application #: 09579596	Filing Dt: 05/26/2000
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53	Patent #: 6535062	Issue Dt: 03/18/2003	Application #: 09607223	Filing Dt: 06/30/2000
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54	Patent #: 6693980	Issue Dt: 02/17/2004	Application #: 09664298	Filing Dt: 09/18/2000
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55	Patent #: 6580383	Issue Dt: 06/17/2003	Application #: 09703646	Filing Dt: 11/01/2000
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56	Patent #: 6975189	Issue Dt: 12/13/2005	Application #: 09705134	Filing Dt: 11/02/2000
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57	Patent #: 6400229	Issue Dt: 06/04/2002	Application #: 09790796	Filing Dt: 02/22/2001
Title: LOW NOISE, LOW DISTORTION RF AMPLIFIER TOPOLOGY				
58	Patent #: 6717450	Issue Dt: 04/06/2004	Application #: 10144175	Filing Dt: 05/13/2002
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59	Patent #: 6683904	Issue Dt: 01/27/2004	Application #: 10144329	Filing Dt: 05/13/2002
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60	Publication #: US20030210737	Pub Dt: 11/13/2003		
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61	Patent #: 6882294	Issue Dt: 04/19/2005	Application #: 10635826	Filing Dt: 08/06/2003
Title: RESISTIVE LADDER, SUMMING NODE CIRCUIT, AND TRIMMING METHOD FOR A SUBRANGING ANALOG TO DIGITAL CONVERTER				
62	Publication #: US20050030216	Pub Dt: 02/10/2005		
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63	Patent #: 6825697	Issue Dt: 11/30/2004	Application #: 10689496	Filing Dt: 10/20/2003
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64	Patent #: NONE	Issue Dt:	Application #: 10698257	Filing Dt: 10/30/2003
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65	Publication #: US20040257125	Pub Dt: 12/23/2004		
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66	Patent #: 7095347	Issue Dt: 08/22/2006	Application #: 10715262	Filing Dt: 11/17/2003
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68	Patent #: 6879276	Issue Dt: 04/12/2005	Application #: 10739860	Filing Dt: 12/18/2003
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65	Publication #: <u>US20050035821</u>	Pub Dt: 02/17/2005		
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67	Publication #: <u>US20050035788</u>	Pub Dt: 02/17/2005		
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68	Publication #: <u>US20050038846</u>	Pub Dt: 02/17/2005		
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69	Publication #: <u>US20050035791</u>	Pub Dt: 02/17/2005		
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71	Publication #: <u>US20050128118</u>	Pub Dt: 06/16/2005		
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72	Publication #: <u>US20050083223</u>	Pub Dt: 04/21/2005		
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73	Publication #: <u>US20050270107</u>	Pub Dt: 12/08/2005		
Title: LOW DISTORTION AMPLIFIER				
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75	Publication #: <u>US20060120479</u>	Pub Dt: 06/08/2006		
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	Patent #: NONE	Issue Dt:	Application #: 11788451	Filing Dt: 04/20/2007
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Title: Dynamic digital pre-distortion system				

Patent #: NONE Issue Dt: Application #: 11951238 Filing Dt: 12/05/2007
78 Publication #: US20080095265 Pub Dt: 04/24/2008
Title: DIGITAL PRE-DISTORTION TECHNIQUE USING NON-LINEAR FILTERS

Patent #: NONE Issue Dt: Application #: 12031249 Filing Dt: 02/14/2008
79 Publication #: US20080211583 Pub Dt: 09/04/2008
Title: SYSTEM AND METHOD FOR DYNAMIC DRAIN VOLTAGE ADJUSTMENT TO CONTROL LINEARITY, OUTPUT POWER, AND EFFICIENCY IN RF POWER AMPLIFIERS

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